

Battery Powered Embedded System in IoT Applications low Power Design Techniques

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ABSTRACT

Nowadays thanks to the Internet of Things (IoT) thing has become so easy and simple to use because everywhere technology has brought connectivity and intelligence to simple things. Battery powered, embedded systems are at the heart of this revolution: the spine of so many products that make up the IoT. By utilizing this type of power management, these systems including ubiquitous wearable fitness trackers and remote environmental sensors can provide decades of operation time while continuously delivering optimum performance. Mastering low power design techniques is essential in developers and engineers working in the IoT domain as demand for IoT applications are increasing. You can say that this comprehensive guide will cover low power design for battery powered embedded systems in the IoT applications. In this second article we will go into the fundamental principles, cutting edge techniques and best practices for creating energy efficient devices, which can work for long periods of time on minimal power sources. Knowing and applying these methods developers can use the technology available in the IoT to push the limits of what is possible in the design and manufacture of IoT devices to truly functional, sustainable, and environmentally friendly.

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AN UNDERSTANDING OF LOW POWER DESIGN IN IoT

IoT applications cannot be overstated the significance of low power design. The demand for energy efficient solutions is becoming more and more important, considering the increasing number of connected devices. Along with being able to enable longer battery life this also means developing low power design that is more sustainable in the sense it reduces the future environmental impact of IoT devices.

Balancing performance with power consumption is one of the main problems of the IoT development. To achieve lasting operations, devices must perform complex tasks at the same time as conserving their energy. To achieve this, though, a holistic approach to design that tethers the two domains – hardware and software – is necessary.

In addition, low power design allows the deployment of IoT devices in remote or remote to

inaccessible locations where regular battery changes are impossible or impractical. The resulting expanded reach opens up new possibilities for data collection, monitoring, and automation in many industries from agriculture to industrial manufacturing.

Low-power design can also improve the overall cost of ownership for IoT devices by prioritizing. Being able to run longer on a single charge means less maintenance and replacement, which can cut costs in big ways during the lifetime of a device. The economic benefit makes IoT more attractive economically to businesses and consumers so that it lends itself as more attractive, particularly to the businesses, and the consumers for further adoption and further innovation in the space of IoT.^[1-4]

LowPower Design Principles for Embedded Systems

Various fundamental principles of successful low power design in battery powered embedded systems

used in IoT applications are presented. The basis for such energy efficient devices is these guiding concepts, which lay out the best performance while consuming the least power.

Concept of power budgeting is one of core principal. A power system is said to be managed when one carefully analyses and allocates power resources among various components and functions in a system. Developers can understand the power requirements from each element to select components, design system architecture, and make operational strategies.

The second key ingredient is the adoption of power aware design practices. The approach provides for considerations around power consumption across all development process stages, from concept through implementation. Developers aimed at inventing systems that are already energy conscious can be achieved by keeping power efficiency as the prime consideration during the design (Figure 1).

Low power design also requires the use of modularity and scalability. By building the modular systems with the interchangeable components, developers can easily leverage power consumption for the specific use case. The flexibility here gives birth to varied solution base that fits with varied power requirements of different IoT products.

The principle of intelligent power management poses a dynamic control of the system's resources. This means putting in place sophisticated algorithms

and algorithms that are capable of applying power usage on the grounds of real time conditions and operational needs, in a way that energy distribution should be continuous.^[5-9]

LOW POWER IoT DEVICES — HARDWARE CONSIDERATIONS

Therefore, appropriate hardware components suitable for low power consumption are selected for battery powered embedded systems for IoT applications. The power requirements and capabilities of each element in the system depend on each component choice, so each and every component choice can make a big difference in the overall energy efficiency of the device.

For IoT devices, you would usually choose ultra low power microcontrollers. They are also energy efficient specialized microcontrollers with multiple power saving mode and efficient sleep/wake mechanism. Some of these advanced microcontrollers even have energy harvesting capabilities to help reduce the need for battery power harvested from the environment.

Sensors are essential to many of the IoT applications, but can also be very power hungry, if not carefully selected, and it is important to understand the tradeoffs involved. We require low power sensors with low standby current and high efficiency active modes to extend battery life. In addition, it is also

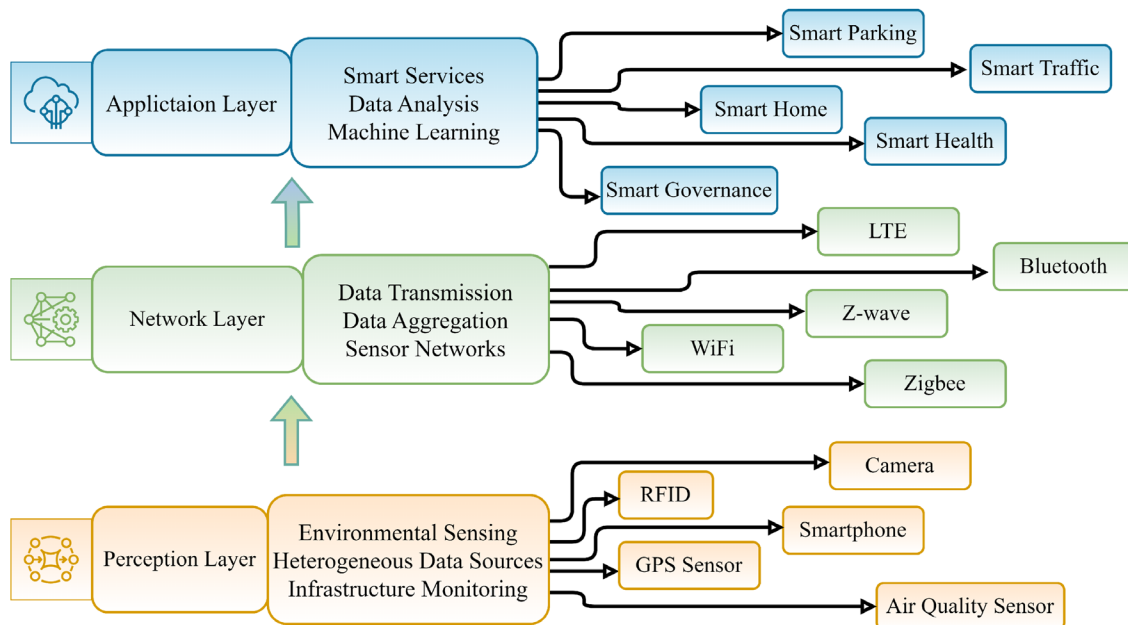


Fig. 1. Low Power Design Principles for Embedded Systems

possible to consider ways sensor fusion techniques can reduce the number of sensors needed more efficiently, further reducing power consumption.

Another important component in low power design are power management integrated circuits (PMICs). They allow voltage levels to be efficiently regulated, power distributed and sophisticated power saving strategies can be implemented by these specialized ICs. Advanced PMICs can help developers make sure every milliwatt is used effectively in the whole system.

Those communication modules are often the most power hungry components of the IoT device. By choosing low power wireless technologies like Bluetooth Low Energy (BLE), LoRaWAN or NB-IoT, transmitting data can require a lot less energy. For IoT applications these technologies are particularly meant for long range communication while consuming less power.^[10-14]

Software Strategies for Minimizing Power Consumption

Once hardware is chosen for low-power design, having software optimized equally well ensures that energy will be conserved efficiently in battery powered embedded systems for IoT applications. In the right hands, efficient design of software can dramatically cut down energy use by figuring out the most efficient ways system resources are used and managed.

Launching power aware algorithms is one of the most efficient way of reducing power consumption. The goal of these algorithms is to reduce f devices by orders of magnitude (a milimillion times!) while still offering ideal performance by implementing advanced power management techniques.

Sleep modes are one of the most fundamental power management techniques and how civilization could have stalled without them. On modern microcontrollers, different levels of sleep are provided, from light sleep states that can quickly wake up to deep sleep modes greatly reducing power consumption. Timing these sleep modes properly, as well as taking advantage of efficient wake up mechanisms can drastically reduce energy usage overall.

Another powerful power management technique is dynamic voltage and frequency scaling (DVFS). The DVFS technique achieves power optimization while maintaining performance requirements with the use of processor operating voltage and clock frequency dynamically adjusted to meet the processor's current workload requirements. Quite insightful in systems that can have different computational demands.

The power gating is a technique which is used to turn on and off a part of the system arbitrarily. This can be applied to entire subsystems of the device as well as to the device itself as well as to individual peripherals or memory banks. Power gating achieves great static power consumption and leakage current reduction, by cutting power to inactive components.

Intelligent battery management system is necessary to advance the battery life and prevent the battery operation by instituting as much control as possible of the charging phase. They can track battery state of health, schedule charging cycles, and automatically activate or deact the features that prevent over charge, over discharge, or overheating. Predictive algorithms that estimate remaining battery life can also be part of advanced battery management

Table 1: Low Power Design Techniques for Battery-Powered Embedded Systems

Technique	Purpose
Dynamic Voltage Scaling	Dynamic voltage scaling reduces the supply voltage of the system dynamically based on workload, improving power efficiency.
Clock Gating	Clock gating disables the clock signal to inactive components, reducing dynamic power consumption in idle states.
Power Gating	Power gating turns off the power supply to unused sections of the system, preventing leakage current and saving energy.
Energy Harvesting	Energy harvesting techniques capture energy from the environment (e.g., solar, vibration) to supplement battery power, extending operational time.
Sleep Modes	Sleep modes reduce power consumption by turning off unnecessary components when the system is not actively processing data.
Voltage Scaling	Voltage scaling adjusts the voltage levels supplied to the system based on performance needs, lowering energy consumption while maintaining functionality.

techniques, and use the estimation result to adjust system behavior in some situations, thus saving battery energy.^[17]

Self Sustaining IoT Devices through Energy Harvesting Solutions

Energy harvesting is an exciting technology that enables the development of self sustaining battery powered embedded systems when used in IoT applications. These solutions capture and convert environmental ambient energy, and in supplement or some times even replace conventional battery power, significantly lengthening the operation life of IoT devices.

One of the most widely used techniques for solar energy harvesting is for outdoor IoT applications. With advances in photovoltaic technology, modern small solar cells have become highly efficient, compact devices that can be integrated to small IoT devices. However, when combined with energy efficient design practice designs, solar harvesting can enable devices to indefinitely operate in appropriate environments.

A type of energy harvesting makes use of temperature differential to produce an electrical power. The technique is particularly useful in industrial IoT contexts where temperature gradients are pervasive. Waste heat from machinery or environmental temperature variations may become usable as electrical energy to power sensors and communication modules in thermoelectric generators.

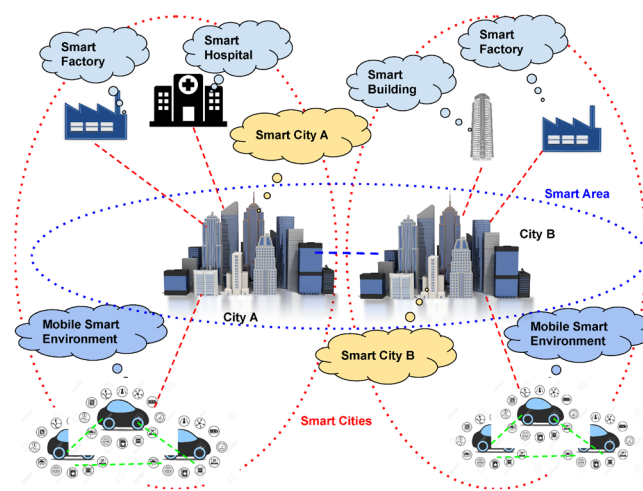


Fig. 2: Self Sustaining IoT Devices through Energy Harvesting Solutions

Kinetic energy harvesting is extracting energy from a motion or vibration to electrical power. All this can be done though a variety of ways including

through the use of piezoelectric materials which generate electricity when stressed or electromagnetic generators that produce power owing to oscillating movements. Since most IoT devices are deployed in similar (i.e. moving) environments, kinetic harvesting is well suited.

Radio frequency (RF) energy harvesting is a young technology emerging for harvesting energy from ambient radio waves. Although limited in power output today, this technology has promise to enable ultra low power IoT devices in urban urban environments, where the RF signal is plentiful.^[18]

COMMUNICATION PROTOCOLS OPTIMIZATION FOR ENERGY EFFICIENCY OPTIMIZATION

Generally speaking, communication is one of the most power intensive operations in IoT devices and optimizing communication protocols is imperative if you want to achieve low power consumption of battery powered embedded systems. Under a careful selection and implementation of energy-efficient communication strategies, IoT device battery life can be greatly extended.

Low power wide area networks (LPWAN) technologies like LoRaWAN and NB-IoT are targeted at IoT applications where those devices need to communicate over long distances using as little power as possible. The methods for adapting data rate and optimizing the schedule for transmission minimize the amount of energy used without unduly reducing the level of reliability required to support connectivity.

Bluetooth low energy (BLE), provides much power saving over traditional Bluetooth for short range communication. BLE makes sense as quick and energy efficient data transport for communication between IoT devices that need to communicate frequently but in small bursts. They also can reduce energy consumption even further by implementing BLE power saving features, primarily scottage intervals and slave latency.

When multiple IoT devices have to exchange data among each other, mesh networking protocols such as Zigbee or Thread can be an energy efficient solution. These protocols permit devices to relay messages over the network to minimize required transmit power in long-range communication and to spread the energy load across multiple devices.

Also reducing the energy used for communication is that data packaging and compression techniques can be implemented in an efficient manner. However, by

reducing the amount of data that must be transmitted and how it's transmitted, developers can minimize the length and frequency of power intensive radio transmissions.^[19-22]

Advances in Thermal Management and the Effect on Power Consumption

Thermal management is an effective but often overlooked aspect of low power design for battery powered embedded systems in Internet of Things applications. Thermal design also is an important contributor to reliable operation as well as an important feature in optimizing power consumption and extending battery life.

Such factors as increased currents leakage in semiconductors, and if we narrow it down further, reduced efficiency on power conversion circuits can lead to significantly higher power consumption even at high temperatures. Thermal management strategies that are efficient can keep the operating temperature at their optimum and minimize these Power losses.

Strategic component placement and use of heat-spreading materials can be very effective in managing thermal problems while consuming little or no additional power. For more demanding applications, active cooling solutions such as small fans or thermoelectric coolers may be required, but they are consuming power and must be carefully balanced with the resulting thermal benefits they bring.

This suggests components and materials should consider thermal factors in their selection. With low thermal resistance components, and thermal aware PCB design, heat dissipation can be greatly enhanced and can not only greatly improve heat dissipation without the use of active cooling measures (Table 2).

Low Power IoT Device Testing and Optimization Strategies

However, to achieve and maintain low power consumption for battery-powered embedded systems in the context of IoT there is a need for rigorous testing as well as continuous optimization. As a result, developers can implement comprehensive testing strategies and iterative optimization processes to make sure that their devices can meet power efficiency goals and effectively work in real world conditions.

The optimization process must begin with power profiling. Using specialized tools and techniques, developers can study their devices power consumption across operation states and conditions. By doing this detailed profiling; we can identify the power hungry components or process which requires optimization.

For that reason, it is important to simulate real world usage scenarios to understand how devices will fare when deployed in the real world. It is a process that involves development of test cases that resemble the normal interactions users have with your app, environments and patterns of communications with other software components. The use of these simulated conditions gives developers the ability to analyze the behavior of device under these conditions and fine tune power management strategies and find issues before deployment.

Another important thing is to test battery life of low power IoT devices. A good projection of battery life is possible by combining power profiling data with expected usage patterns in order to get an accurate model for this. This is really important in establishing realistic expectations and knowing what to expect

Table 2: Key Parameters for Low-Power IoT Design in Battery-Powered Systems

Parameter	Consideration
Power Consumption	Power consumption should be minimized by optimizing the design of the system and ensuring low standby power consumption.
Duty Cycle	Duty cycle management helps control the time the system is active versus idle, optimizing power use during operation.
Energy Efficiency	Energy efficiency measures ensure that the system uses minimal energy during both active and idle states to extend battery life.
Component Selection	Component selection focuses on choosing low-power components that meet performance requirements while minimizing overall system power usage.
Wake-up Time	Wake-up time must be optimized for low-power IoT systems to ensure the system can quickly resume operation from low-power modes when required.
Battery Life	Battery life is extended by employing power-saving techniques such as sleep modes, low-power components, and energy harvesting, ensuring long-term system functionality.

so you know if devices will sustain their intended operational lifespan.

This should extend beyond development and monitoring could be continuous. As the lifecycles of devices continue, implementing OTA update capabilities lets you keep optimizing firmware, and fix bugs, to ensure devices remain at peak power efficiency over their lifetimes.

LOW POWER IoT DEVICES REGULATORY CONSIDERATIONS AND STANDARDS.

Regulatory considerations and industry standards have become an increasingly important part of the IoT landscape as the IoT landscape continues to evolve, where low power embedded systems for IoT applications are concerned. Following through on these regulations and standards; not only does it guarantee compliance, but can also help developers develop in an energy efficient fashion.

ENERGY STAR for connected device energy efficiency certifications establish energy efficiency guidelines for energy consumption for different IoT products such as TVs, PC's, monitors, servers and more. Designing devices to beat or even match, on several occasions exceeding these standards, demonstrates a developer's commitment to energy efficiency and possibly grant a competitive advantage in the market.

The design of wireless communication systems in IoT devices is severely affected by radio frequency regulations, which depend on a region. Many of these regulations also require optimization of transmission power and duty cycles, which fit with the low power design goals. Having a good understanding of these regs, and being willing to follow them before beginning will help alleviate costly redesigns and make objects globally compatible.

Power consumption and costs are not inherently tied to the power consumption, but rather data privacy and security standards are. Increased processing power is often needed to implement robust security measures, but doing so may conflict with objectives of energy efficiency. Security requirements have to be met, and developers have to find ways of meeting them without increasing power consumption much.

Matter is an emerging standards for IoT interoperability with the objective to have a united ecosystem of connected devices. These standards are first of a less requirement, but also of a more using standardized communication protocols and power management methods to optimize power consumption.

FUTURE TRENDS IN LOW POWER DESIGN FOR IoT APPLICATIONS

New technologies and approaches appear constantly in the field of low power design for battery powered embedded systems on IoT applications. Building understanding around these trends will allow developers to stay ahead of the curve and build more efficient and powerful IoT devices. Traditionally, IoT device power management involves advanced tasks, such as predicting machine behavior and device parameters, time, and changes before power is removed. Running AI algorithms on IoT devices directly can allow for more sophisticated power saving strategies, using real time analysis of usage patterns and environmental conditions, which can be attained through edge AI. Since they will increase the energy density and overall performance of battery powered IoT device new battery technologies such as solid state batteries and advanced lithium ion formulations are being developed. It may also mean the capacity for smaller devices with longer operational lifetime. Future improvements in the energy efficiency of IoT devices will continue to be driven by advancements into semiconductor technology such as development of more efficient transistors built into SoC designs with power management incorporated directly into the system.

CONCLUSION

Energy harvesting technologies for powering 'battery free' IoT devices are becoming more mainstream. With the improvements in energy harvesting techniques and energy storage technologies things can change in the direction, where the self sustaining IoT ecosystems with small external power input can come into being. For example, if we are looking to the future, the emphasis will continue to be on low power design so we can realize the full power of IoT technology. As developers keep abreast of these trends and keep improving their energy efficient design approach, they introduce new IoT solutions that challenge the status quo in terms of functionality, reliability and sustainability. Finally, for successful creation of IoT applications we need to master low power design techniques for battery powered embedded systems. Developers of IoT can use right hardware selection, efficient software strategies and strong reliance on state of the art power management traits to come up with devices that fulfill the functional needs of IoT

apps and can work reliably as well as efficiently over scarce power sources. The growing, and continuing, evolution of the IoT means that energy efficient design is only going to become more important, making these skills and knowledge crucial to anyone working in this exciting and rapidly developing area.

REFERENCES:

- Hassan, S., Dhali, M., Zaman, F., & Tanveer, M. (2021). Big data and predictive analytics in healthcare in Bangladesh: regulatory challenges. *Heliyon*, 7(6).
- He, Y., Eguren, D., Luu, T. P., & Contreras-Vidal, J. L. (2017). Risk management and regulations for lower limb medical exoskeletons: a review. *Medical Devices: Evidence and Research*, 89-107.
- Iqbal, M. H., Aydin, A., Brunckhorst, O., Dasgupta, P., & Ahmed, K. (2016). A review of wearable technology in medicine. *Journal of the Royal Society of Medicine*, 109(10), 372-380.
- Benini, L., Bogliolo, A., & De Micheli, G. (2000). A survey of design techniques for system-level dynamic power management. *IEEE transactions on very large scale integration (VLSI) systems*, 8(3), 299-316.
- Vallabhuni, R. R., Sravana, J., Saikumar, M., Sriharsha, M. S., & Rani, D. R. (2020, August). An advanced computing architecture for binary to thermometer decoder using 18nm FinFET. In *2020 Third International Conference on Smart Systems and Inventive Technology (ICSSIT)* (pp. 510-515). IEEE.
- Bernardi, P., Rebaudengo, M., & Reorda, S. (2004, September). Using infrastructure IPs to support SW-based self-test of processor cores. In *Fifth International Workshop on Microprocessor Test and Verification (MTV'04)* (pp. 22-27). IEEE.
- Bernstein, J. B., Gurfinkel, M., Li, X., Walters, J., Shapira, Y., & Talmor, M. (2006). Electronic circuit reliability modeling. *Microelectronics Reliability*, 46(12), 1957-1979.
- Bertogna, M., Cirinei, M., & Lipari, G. (2005, July). Improved schedulability analysis of EDF on multiprocessor platforms. In *17th Euromicro Conference on Real-Time Systems (ECRTS'05)* (pp. 209-218). IEEE.
- Grigorescu, S., Trasnea, B., Cocias, T., & Macesanu, G. (2020). A survey of deep learning techniques for autonomous driving. *Journal of field robotics*, 37(3), 362-386.
- Prasad, S. V. S., Pittala, C. S., Vijay, V., & Vallabhuni, R. R. (2021, July). Complex Filter Design for Bluetooth Receiver Application. In *2021 6th International Conference on Communication and Electronics Systems (ICCES)* (pp. 442-446). IEEE.
- Guo, H., Low, K. S., & Nguyen, H. A. (2009). Optimizing the localization of a wireless sensor network in real time based on a low-cost microcontroller. *IEEE transactions on industrial electronics*, 58(3), 741-749.
- Hammoudi, K., Benhabiles, H., Kasraoui, M., Ajam, N., Dornaika, F., Radhakrishnan, K., ... & Liu, S. (2015). Developing vision-based and cooperative vehicular embedded systems for enhancing road monitoring services. *Procedia Computer Science*, 52, 389-395.
- Meier, L., Honegger, D., & Pollefeys, M. (2015, May). PX4: A node-based multithreaded open source robotics framework for deeply embedded platforms. In *2015 IEEE international conference on robotics and automation (ICRA)* (pp. 6235-6240). IEEE.
- Sun, Z., Feng, B., Lu, L., & Jha, S. (2020, May). OAT: Attesting operation integrity of embedded devices. In *2020 IEEE Symposium on Security and Privacy (SP)* (pp. 1433-1449). IEEE.
- Sun, Z., Feng, B., Lu, L., & Jha, S. (2020, May). OAT: Attesting operation integrity of embedded devices. In *2020 IEEE Symposium on Security and Privacy (SP)* (pp. 1433-1449). IEEE.
- Abbasi, A., Holz, T., Zambon, E., & Etalle, S. (2017, December). ECFI: Asynchronous control flow integrity for programmable logic controllers. In *Proceedings of the 33rd Annual Computer Security Applications Conference* (pp. 437-448).
- Abbasi, A., Holz, T., Zambon, E., & Etalle, S. (2017, December). ECFI: Asynchronous control flow integrity for programmable logic controllers. In *Proceedings of the 33rd Annual Computer Security Applications Conference* (pp. 437-448).
- Monmasson, E., Idkhajine, L., Cirstea, M. N., Bahri, I., Tisan, A., & Naouar, M. W. (2011). FPGAs in industrial control applications. *IEEE Transactions on Industrial informatics*, 7(2), 224-243.
- Pang, Y., Yuan, Y., Li, X., & Pan, J. (2011). Efficient HOG human detection. *Signal processing*, 91(4), 773-781.
- Viola, P., & Jones, M. J. (2004). Robust real-time face detection. *International journal of computer vision*, 57, 137-154.
- Nguyen, X. S., Brun, L., L  zoray, O., & Bougleux, S. (2019). A neural network based on SPD manifold learning for skeleton-based hand gesture recognition. In *Proceedings of the IEEE/CVF Conference on Computer Vision and Pattern Recognition* (pp. 12036-12045).
- Liu, J., Liu, Y., Wang, Y., Prinnet, V., Xiang, S., & Pan, C. (2020). Decoupled representation learning for skeleton-based gesture recognition. In *Proceedings of the IEEE/CVF conference on computer vision and pattern recognition* (pp. 5751-5760).
- Zakaria, R., & Zaki, F. M. (2024). Vehicular ad-hoc networks (VANETs) for enhancing road safety and efficiency. *Progress in Electronics and Communication Engineering*, 2(1), 27-38. <https://doi.org/10.31838/PECE/02.01.03>
- Uvarajan, K. P. (2024). Integration of blockchain technology with wireless sensor networks for enhanced IoT security. *Journal of Wireless Sensor Networks and IoT*, 1(1), 23-30. <https://doi.org/10.31838/WSNIOT/01.01.04>

25. Uvarajan, K. P. (2024). Advances in quantum computing: Implications for engineering and science. *Innovative Reviews in Engineering and Science*, 1(1), 21-24. <https://doi.org/10.31838/INES/01.01.05>
26. Sadulla, S. (2024). Techniques and applications for adaptive resource management in reconfigurable computing. *SCCTS Transactions on Reconfigurable Computing*, 1(1), 6-10. <https://doi.org/10.31838/RCC/01.01.02>
27. Geetha, K. (2024). Advanced fault tolerance mechanisms in embedded systems for automotive safety. *Journal of Integrated VLSI, Embedded and Computing Technologies*, 1(1), 6-10. <https://doi.org/10.31838/JIVCT/01.01.02>
28. Sathish Kumar, T. M. (2023). Wearable sensors for flexible health monitoring and IoT. *National Journal of RF Engineering and Wireless Communication*, 1(1), 10-22. <https://doi.org/10.31838/RFMW/01.01.02>
29. Santhosh, M., Kavitha, S., Keerthana, R., Suganya, L., & Krishnakumar, S. (2016). Electronic voting machine using internet. *International Journal of Communication and Computer Technologies*, 4(2), 72-75.
30. Salameh, A. A., & Mohamed, O. (2024). Design and Performance Analysis of Adiabatic Logic Circuits Using Fin-FET Technology. *Journal of VLSI Circuits and Systems*, 6(2), 84-90. <https://doi.org/10.31838/jvcs/06.02.09>
31. Sindhu, C. K., Sowmya, A. N., Haveela, B., & Kavya Nandini, G. (2021). Design of frequency reconfigurable microstrip antenna. *National Journal of Antennas and Propagation*, 3(1), 16-21.